

# Protectli Appliance

Protectli Vault Pro VP2440  
Intel® N150 Quad-Core Processor  
2x X710-BM2 10GbE SFP+  
2x I226-V 2.5GbE Ports

February 27th, 2026

# Overview

The Protectli Vault Pro VP2440 is the first 4-Port Vault Pro model featuring SFP+ ports, allowing for the connectivity of 10GbE modules while maintaining a smaller, fanless form factor. The VP2440 is powered by a quad-core Intel® N150 Processor with 32GB onboard eMMC storage and supports up to 64GB of DDR5 SO-DIMM RAM. Keyed M.2 slots are available for PCIe Gen 3x2 NVMe SSD storage, a WiFi card (E key, up to 3052 form factor), and a 5G/LTE module (B Key, 3052 form factor). The VP2440 is equipped with 2 Intel® I226-V RJ-45 Ethernet ports, supporting up to 2.5GbE speeds with full backward compatibility to 1GbE and lower as well as two Intel® X710 BM2 10GbE SFP+ ports, supporting a wide range of SFP+ modules.

Protectli Vaults utilize Intel components to ensure persistent compatibility with a wide range of operating systems (OS) and applications. The VP2440 features a fanless, all-aluminum chassis design, allowing for efficient heat dissipation from the CPU and other components without any moving parts or additional power requirements. Internal heatsinks and thermal pads are supplied to provide optional additional passive cooling for the NVMe, SFP+ Ports, RAM, WiFi, and 4G/5G modem modules.

## Technical Specifications

<b>Model</b>	VP2440
<b>Description</b>	2x 10GbE SFP+, 2x 2.5GbE Network Port Appliance
<b>Processor</b>	Intel® N150 Quad Core Processor (6MB Cache, up to 3.6GHz)
<b>Microarchitecture</b>	x86_64-v3
<b>Processor Cores</b>	4
<b>Processor Threads</b>	4
<b>Intel® AES-NI</b>	Supported
<b>Virtualization</b>	Intel® Vt-x, Vt-d
<b>Network</b>	2x Intel® X710-BM2 SFP+, 2x Intel® I226-V 2.5G Ethernet (RJ-45)
<b>Video / Graphics</b>	Intel® HD Graphics GPU, 24 Execution Units, Max Dynamic Frequency 1GHz, HDMI 2.1, DP 1.4
<b>Max Resolution</b>	3840 x 2160 @60hz
<b>Audio</b>	Audio over HDMI, Display Port, and USB-C Display Port
<b>Memory</b>	1x SO-DIMM DDR5-4800, Max 64GB
<b>Onboard Storage</b>	1x M.2 2280 NVMe, 1x 32G eMMC on board
<b>Optional Additional Storage</b>	Not Supported
<b>External I/O</b>	2x 2.5GbE RJ-45 Ethernet ports

	2x 10GbE SFP+ Ports
	4x USB 2.0 Type-A
	1x USB 3.2 Gen 2 Type-C with DisplayPort
	1x USB Type-C COM Port
	1x HDMI 2.1 Port
	1x Display Port 1.4
	1x Nano (4FF) SIM Holder
	8x WiFi/LTE Antenna Mounting Holes
	1x 12V DC Power Jack
<b>Internal I/O</b>	1x M.2 2280 M-Key PCIe 3.0 x2 (NVMe)
	1x M.2 2230/3030/3052 E-Key PCIe 3.0 x1 (WiFi)
	1x M.2 3052 B-Key USB 3.2 Gen 1 (4G/5G Modem)
	1x CPU Fan Header (4 pin) (1.25mm pitch)
	1x JNTP1 Header (NTP/i2c) (4 pin) (1.25mm pitch)
	1x GPIO Header (2x3 pin) (2.54mm pitch)
	1x eSPI Header (2x5 pin, pin 10 clipped) (2.00mm pitch)
	1x RS-232 COM Header (2x5 pin, pin 10 clipped) (2.00mm pitch)
	1x Front Panel Header (2x5 pin, pin 10 clipped) (2.54mm pitch)
	1x CMOS Reset (2 pin)
	1x BIOS Programming Header (Two separate 1x4 pin) (2.00mm pitch)
<b>Super I/O Chip</b>	IT8659E
<b>BIOS</b>	AMI® or coreboot
<b>Indicators</b>	1x LED Power Button (Blue), 1xLED Power Indicator (Green), 1x LED SSD Activity Indicator (Yellow)
<b>Power</b>	Input 12V DC, 1x DC Power Jack
<b>Power Usage</b>	Max 45W
<b>Chassis</b>	Fanless, Aluminum, Gray
<b>Chassis Dimensions</b>	170mm x 152mm x 53mm
<b>Mounting Options</b>	Desktop, VESA Bracket, Optional 1RU Rack Mount
<b>Weight</b>	3lbs, 7.38oz (1.57kg)
<b>Shipping Weight</b>	5lbs, 3.54oz (2.37kg)
<b>Operating Temperature</b>	+14° - +122° F, -10° - +50° C

<b>Operating Humidity</b>	0 – 95% relative humidity, non-condensing
<b>Approvals</b>	UL (Power Supply), FCC Part 15 Class B, CE, RoHS
<b>Country of Origin</b>	Made in China, Assembled in USA, Canada, or Germany
<b>Optional TPM</b>	1x Trusted Platform Module, TPM 2.0
<b>NDA Compliance</b>	Compliant

## Included Accessories and Components

60W (12V @5.0A) Power Supply with threaded barrel connector

Power Cable (US/CA, EU, UK, or AUS/NZ)

USB Type-C (with Type-A adapter) to USB Type-C Serial Console Cable

4x M2 component mounting screws (M2 screw size, 4.75mm length, 1.9mm thread diameter)

8x Thermal Pads for use with attached heatsink<sup>†</sup>

Quick Start Guide

<sup>†</sup>More heatsink, thermal pad, and screw details are outlined in the [Thermal Pads and Screws Information](#) towards the end of the datasheet.

## Firmware Support Information

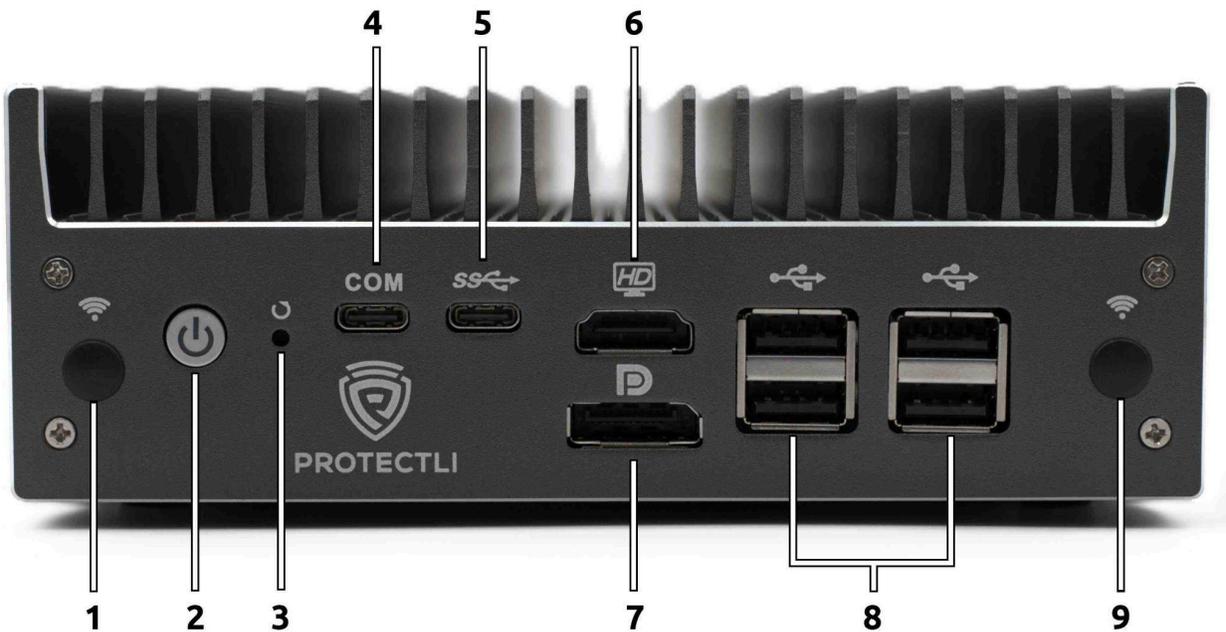
The VP2440 supports American Megatrends, Inc. (AMI) and coreboot firmware. Both firmware options operate exclusively in UEFI mode. They do not support legacy BIOS.

All mentions of the “AMI or coreboot firmware menu” in this document refers to the UEFI setup menu accessed by holding the Delete key at the time of boot. To access the boot options menu, hold the F11 key instead.

Firmware information and downloads for all Protectli Vaults can be found in the [Protectli Knowledge Base](#) on our [coreboot page](#) and [AMI page](#) and are available through [our GitHub](#). We recommend using the Protectli Flashli tool, available through our [GitHub](#), to update Vault firmware.

# External Interfaces

## Front Panel Configuration



Item #	Object	Label	Description
1,9	Antenna Ports		Two antenna ports for adding radio antennas (WiFi, LTE, etc.). The ports are covered by plugs while not in use.
2	Power Button		Pressing the Power Button will power the unit on and illuminate with a blue LED.  <i>In OSes configured to handle ACPI signals, pressing the power button initiates a shutdown.</i>  <i>Pressing and holding the Power Button for 5 seconds will force the unit to power off.</i>
3	Reset Button (Recessed)		A momentary switch exposed via GPIO. This is not an ACPI reset button, but a general purpose button that may be programmed in the guest OS.

4	USB-C COM Port	<b>COM</b>	RS-232 serial communications via USB-C. Default port settings: <ul style="list-style-type: none"> <li>• 115200 baud</li> <li>• No parity</li> <li>• 8 databits</li> <li>• 1 stopbit</li> </ul> This COM port is designated as "COM0" in the AMI Firmware menu. The settings are found at Advanced > Serial Port Console Redirection.
5	USB-C Port		One USB 3.2 Gen 2 <sup>†</sup> Type-C connector with Display Port. (Theoretical maximum throughput of 10Gbps [~1.2GBps]) Supports video/audio output when used as Display Port. <sup>##</sup>
6	HDMI Connector		Video and audio output via HDMI. (HDMI 2.1, max resolution of 3840 x 2160 @60hz) <sup>##</sup>
7	DisplayPort Connector		Video and audio output via DisplayPort. (Display Port 1.4, max resolution of 3840 x 2160 @60Hz) <sup>##</sup>
8	Four USB2 Connectors		USB 2.0 Type-A connectors. (Theoretical maximum throughput of 480Mbps [~60MBps])

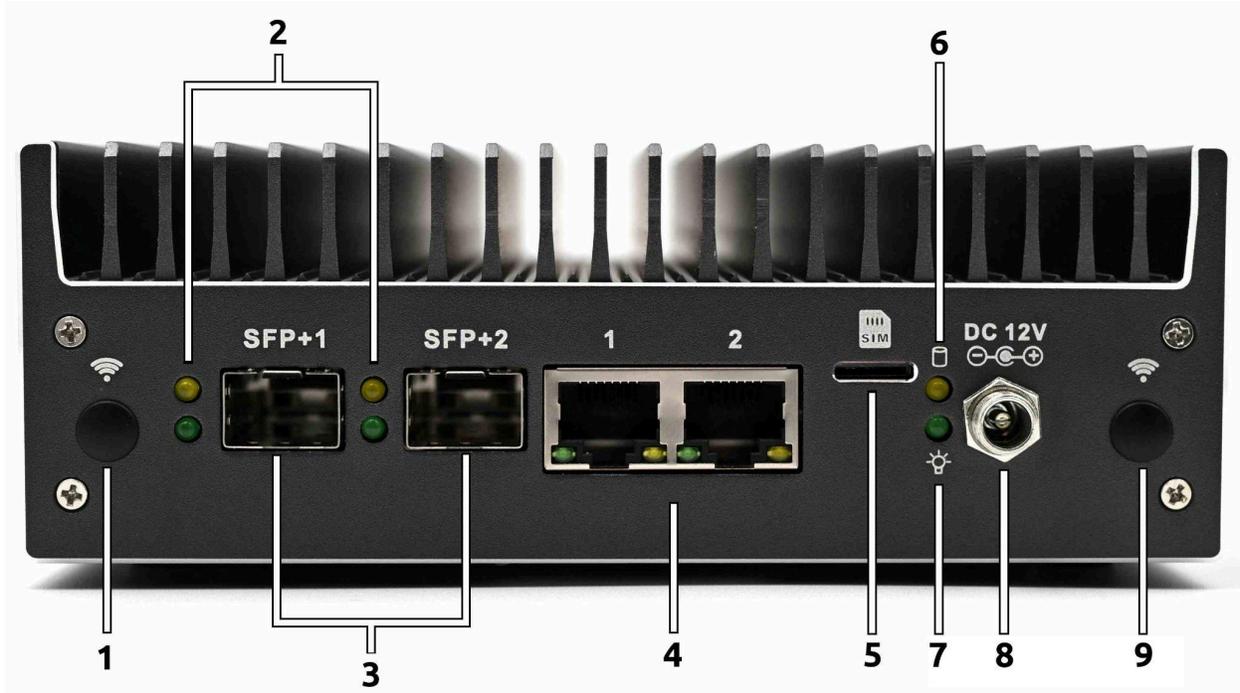
<sup>†</sup>USB-IF naming standard for USB transfer rates: "USB 3.2 Gen 2" is the equivalent and current name for "USB 3.1 Gen 2" offering a theoretical maximum speed of 10 Gigabits (~1.2GB) per second. Older kernels and operating systems may not report the most recent naming convention. For a full linguistic deep dive, please see 3.1 and 3.2 Specification Language Usage Guidelines from USB-IF.

[https://www.usb.org/sites/default/files/usb\\_3\\_2\\_language\\_product\\_and\\_packaging\\_guidelines\\_final.pdf](https://www.usb.org/sites/default/files/usb_3_2_language_product_and_packaging_guidelines_final.pdf),  
[https://www.usb.org/sites/default/files/usb\\_3\\_1\\_language\\_product\\_and\\_packaging\\_guidelines\\_final\\_0.pdf](https://www.usb.org/sites/default/files/usb_3_1_language_product_and_packaging_guidelines_final_0.pdf)

<sup>##</sup>Audio output via HDMI, Display Port, or USB-C Display Port may not work for some operating systems by default. For example: Ubuntu requires the underlying Linux Kernel to be 6.12 or newer. Windows may require you to update all [Intel® Chipset drivers](#), and may require you to update the [Intel® Graphics drivers](#).

Additionally, the maximum display resolution may be locked at 800x600 on some operating systems until the proper driver updates are performed. Again, Ubuntu would require Linux Kernel 6.12 or newer. Windows may require the aforementioned Intel® Chipset drivers & Intel® Graphics drivers.

## Rear Panel Configuration



Item #	Object	Label	Description
1, 9	Antenna Ports		Two (2) antenna ports for adding radio antennas (WiFi, LTE, etc.). The ports are covered by plugs while not in use.
2	SFP+ Activity Lights		Top light will illuminate an orange/amber color when connected at 10GbE, LED will be off when at 1GbE. The bottom light illuminates/blinks green when connected.
3	SFP+ Cages	<b>SFP+1, SFP+2</b>	Two (2) Intel X710-BM2 1/10GbE SFP+ Cages.
4	Ethernet Ports	<b>1, 2</b>	Two (2) 10/100/1000/2500 Mbps Intel® I226-V ethernet ports. LEDs on the left side of NIC emit solid green when connected at 2500/1000Mbps, and are turned off at 100/10Mbps.
5	SIM Slot		Nano (4FF) SIM slot for providing a SIM card to an optional internal cellular modem.
6	Data Activity LED		LED emits yellow when data activity is detected over the NVMe interface.

7	Power Indicator LED		This LED will stay solid green when the device is powered on.
8	Power Supply Connector	<b>DC 12V</b> 	12V DC threaded barrel connector for the 60W external power supply. Positive rail is the tip, negative is sleeve.

## Side Panel Features



Item #	Object	Label	Description
1	Antenna Ports		Two (2) antenna ports on the left and right side of the unit (totalling for an additional four). Used for mounting radio antennas (e.g. WiFi, 4G/5G Modem). The ports are covered by plugs while not in use.

# Internal Interfaces and Components

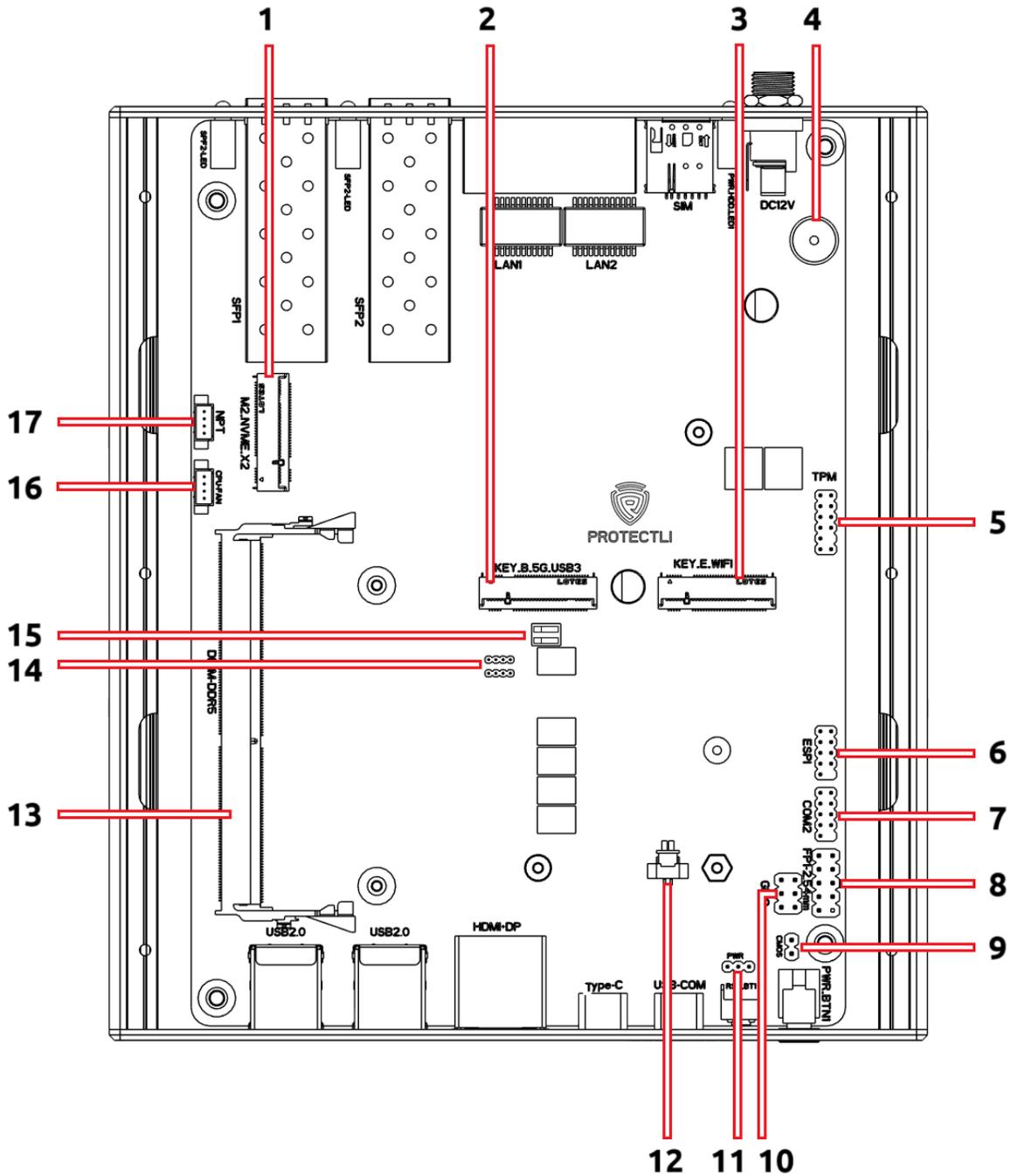


Figure 1. VP2440 motherboard obj file rendering

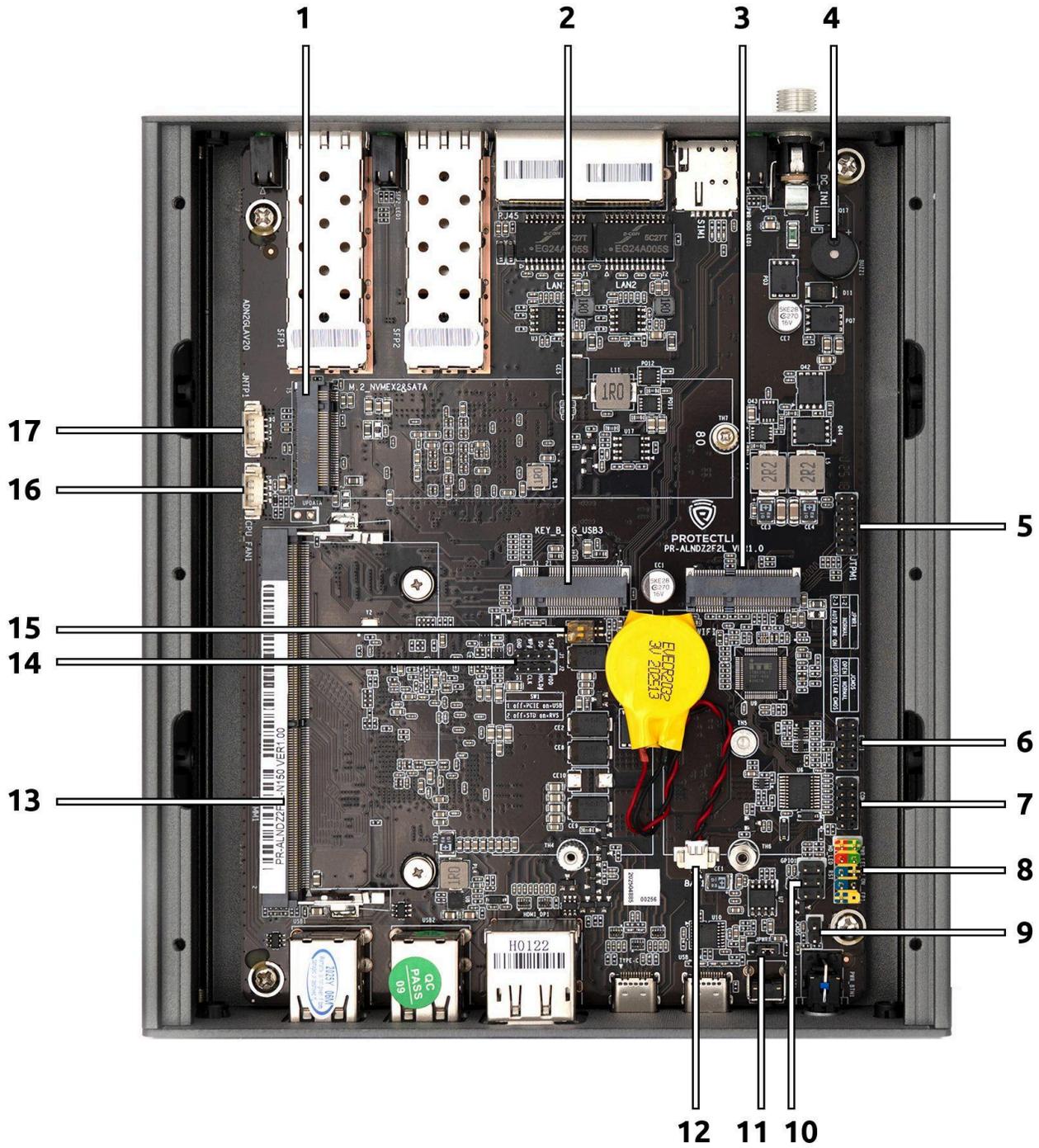


Figure 2. VP2440 actual motherboard image

Item #	Object	Label	Description												
1	M.2 NVMe SSD Connector	M2_NVME_X2	M.2 2280 M-Key connector for a M.2 NVMe SSD that uses PCIe Gen 3.0 x2 protocol. It is designed for an NVMe storage device, but is otherwise a functional two-lane PCIe port.												
2	M.2 4G/5G Modem Connector	KEY_B_5G	M.2 3052 B-Key connector that uses USB 3.2 Gen 1 protocol via M.2 3052 B-Key. Certain behaviors can be affected by the SW1 Switch (Item #15) to assist with compatibility with certain modems. Designed for Protectli cellular modems, but is not limited in its capabilities.												
3	M.2 WiFi Card Connector	KEY_E_WIFI	Connector uses PCIe 3.0 x1 protocol over an M.2 E-Key socket. Designed for WiFi modules, but is not limited in its capabilities. The standoff mount can be moved to support either a 2230/3030 or 3052 sized M.2 WiFi card.												
4	Buzzer	BUZZ1	PC speaker. Produces “beep” sounds that may be utilized by system firmware or certain operating systems.												
5	TPM	JTPM1	<p>Trusted Platform Module header for a TPM2.0 hardware device. (2x6, 2.0mm pitch)</p> <table border="1" data-bbox="722 1024 1409 1386"> <tbody> <tr> <td>Pin 1: VDD</td> <td>Pin 2: TPM_CS#</td> </tr> <tr> <td>Pin 3: SPI_MISO</td> <td>Pin 4: SPI_MOSI</td> </tr> <tr> <td>Pin 5: NC1</td> <td>Pin 6: SPI_CLK</td> </tr> <tr> <td>Pin 7: GND</td> <td>Pin 8: SPI_REST</td> </tr> <tr> <td>Pin 9: NC2</td> <td>X</td> </tr> <tr> <td>Pin 11: NC3</td> <td>Pin 12: TPM_PIRQ#</td> </tr> </tbody> </table> <p>When using a physical TPM connected to this header, verify that dTPM is selected as the TPM Selection in the AMI Firmware menu. This is found at Advanced &gt; PTT Configuration &gt; TPM Device Selection.</p> <p>On coreboot firmware, the physical dTPM will be automatically detected and enabled.</p>	Pin 1: VDD	Pin 2: TPM_CS#	Pin 3: SPI_MISO	Pin 4: SPI_MOSI	Pin 5: NC1	Pin 6: SPI_CLK	Pin 7: GND	Pin 8: SPI_REST	Pin 9: NC2	X	Pin 11: NC3	Pin 12: TPM_PIRQ#
Pin 1: VDD	Pin 2: TPM_CS#														
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Pin 5: NC1	Pin 6: SPI_CLK														
Pin 7: GND	Pin 8: SPI_REST														
Pin 9: NC2	X														
Pin 11: NC3	Pin 12: TPM_PIRQ#														
6	eSPI Header (Enhanced Serial Peripheral Interface)	ESPI1	Header used for low-power, high-speed communication between embedded controllers and other system components. Commonly used for BIOS chip flashing. (2x5, pin 10 clipped, 2.00mm pitch)												

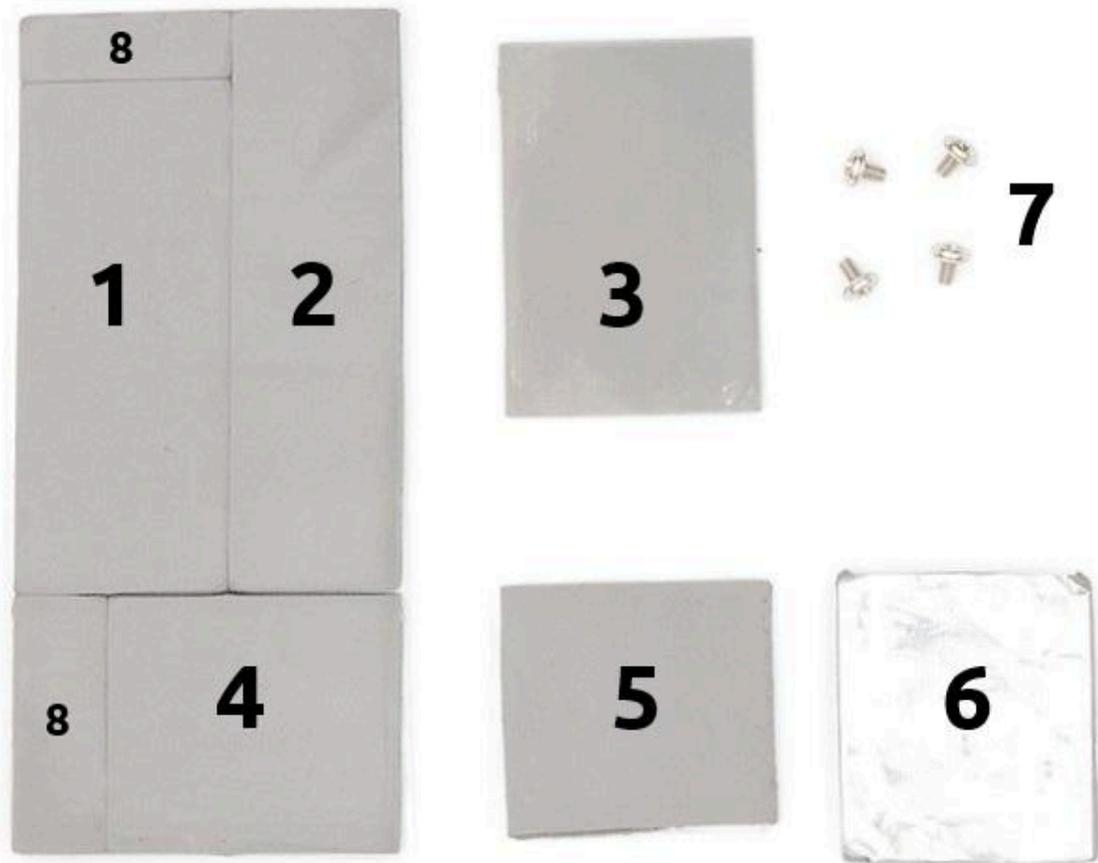
Item #	Object	Label	Description										
7	Serial COM Header	COM2	<p>Header used for serial input/output using RS-232 serial communication (~±12V logic levels). (2x5, pin 10 clipped, 2.00mm pitch)</p> <table border="1" data-bbox="722 478 1409 903"> <tr> <td>Pin 1: Data Carrier Detect - Handshaking Signal</td> <td>Pin 2: Serial In (RX/Receive Data)</td> </tr> <tr> <td>Pin 3: Serial Out (TX/Transmit Data)</td> <td>Pin 4: Data Terminal Ready - Control Signal</td> </tr> <tr> <td>Pin 5: Ground</td> <td>Pin 6: Data Set Ready - Handshaking Signal</td> </tr> <tr> <td>Pin 7: Request to Send - Flow Control</td> <td>Pin 8: Clear to Send - Flow Control</td> </tr> <tr> <td>Pin 9: Ring Indicator</td> <td>X</td> </tr> </table> <p>If utilizing AMI firmware, and you want to use this header strictly for <b>console serial output</b>, make sure that COM1 Console Redirection is Enabled in the AMI firmware menu. This can be found at Advanced &gt; Serial Port Console Redirection. COM0 Console Redirection (the USB-C COM port) may need to be disabled.</p> <p>If utilizing coreboot as your firmware, and you want to use this header strictly for <b>console serial output</b>, access the coreboot firmware menu, go to Dasharo System Features &gt; Serial Port Configuration, and make sure COM1 is enabled. You may need to disable COM0 (the USB-C COM port).</p>	Pin 1: Data Carrier Detect - Handshaking Signal	Pin 2: Serial In (RX/Receive Data)	Pin 3: Serial Out (TX/Transmit Data)	Pin 4: Data Terminal Ready - Control Signal	Pin 5: Ground	Pin 6: Data Set Ready - Handshaking Signal	Pin 7: Request to Send - Flow Control	Pin 8: Clear to Send - Flow Control	Pin 9: Ring Indicator	X
Pin 1: Data Carrier Detect - Handshaking Signal	Pin 2: Serial In (RX/Receive Data)												
Pin 3: Serial Out (TX/Transmit Data)	Pin 4: Data Terminal Ready - Control Signal												
Pin 5: Ground	Pin 6: Data Set Ready - Handshaking Signal												
Pin 7: Request to Send - Flow Control	Pin 8: Clear to Send - Flow Control												
Pin 9: Ring Indicator	X												

Item #	Object	Label	Description										
8	Front Panel Header	FP1	<p>Internal header for adding external device controls and indicators featured through the front panel, such as power button, reset button, activity LEDs, etc. The pinout chart below has been colored to match the baseboard. (2x5, pin 10 clipped, 2.54mm pitch)</p> <table border="1"> <tr> <td>Pin 1: M.2 SSD_LED+ [+3.3V]</td> <td>Pin 2: PWR_LED+ [+3.3V]</td> </tr> <tr> <td>Pin 3: M.2 SSD_LED-</td> <td>Pin 4: PWR_LED-</td> </tr> <tr> <td>Pin 5: Reset_SW +</td> <td>Pin 6: Power_SW +</td> </tr> <tr> <td>Pin 7: Reset_SW -</td> <td>Pin 8: Power_SW -</td> </tr> <tr> <td>Pin 9: No connection</td> <td>X</td> </tr> </table>	Pin 1: M.2 SSD_LED+ [+3.3V]	Pin 2: PWR_LED+ [+3.3V]	Pin 3: M.2 SSD_LED-	Pin 4: PWR_LED-	Pin 5: Reset_SW +	Pin 6: Power_SW +	Pin 7: Reset_SW -	Pin 8: Power_SW -	Pin 9: No connection	X
Pin 1: M.2 SSD_LED+ [+3.3V]	Pin 2: PWR_LED+ [+3.3V]												
Pin 3: M.2 SSD_LED-	Pin 4: PWR_LED-												
Pin 5: Reset_SW +	Pin 6: Power_SW +												
Pin 7: Reset_SW -	Pin 8: Power_SW -												
Pin 9: No connection	X												
9	NVRAM Reset Jumper	JCMOS	<p>Shorting this jumper while the CMOS battery is connected will reset the BIOS NVRAM. This will revert firmware settings to defaults and can assist with certain hardware issues. This will not reset the coreboot admin password if it has been enabled/set.</p>										
10	GPIO Header	GPIO1	<p>Header used for GPIO connections. (2x3 pin, 2.54mm pitch)</p> <table border="1"> <tr> <td>Pin 1: +5V</td> <td>Pin 2: Ground</td> </tr> <tr> <td>Pin 3: GPIO 56</td> <td>Pin 4: GPIO 57</td> </tr> <tr> <td>Pin 5: GPIO 60</td> <td>Pin 6: GPIO 61</td> </tr> </table> <p>On the AMI firmware menu, navigate to Advanced &gt; IT8659 Super IO Configuration &gt; GPIO Configuration to modify the voltage modes for each GPIO pin. You can select between "Output Low", "Output High", and "Input". Output Low will register at ~0.0014V while Output High will register at ~5.10V.</p> <p>On coreboot, there are no settings in the firmware menu to manipulate GPIO behavior. The four GPIO pins will register at ~5.10V.</p>	Pin 1: +5V	Pin 2: Ground	Pin 3: GPIO 56	Pin 4: GPIO 57	Pin 5: GPIO 60	Pin 6: GPIO 61				
Pin 1: +5V	Pin 2: Ground												
Pin 3: GPIO 56	Pin 4: GPIO 57												
Pin 5: GPIO 60	Pin 6: GPIO 61												

Item #	Object	Label	Description								
11	Power Restore Jumper	JPWR1	<p>Jumper setting determines system state after power loss. Based on the orientation in the image above, the <b>default location</b> for the jumper is on the <b>right and middle pins</b> and the Vault will automatically attempt to power back on after power loss on both AMI and coreboot.</p> <p>On the AMI firmware menu, you can change the behavior of the system state after power loss by navigating to Advanced &gt; System Power Management. When "Restore On AC Power Loss" is set to "Power On", the Vault will always attempt to power back on after power loss regardless of the jumper's position on the JPWR1 header. If set to "Power Off", the default jumper location will override the firmware, and will still attempt to power back on. If you wish for the Vault to not automatically power back on, you will need to move the jumper to the left and middle pins while "Restore On AC Power Loss" is set to "Power Off".</p> <p>On the coreboot firmware menu, you can change the power state after power loss by navigating to Dasharo System Features &gt; Power Management Options. You can change the "Power state after power loss" between "Powered Off", "Powered On", or "state at the moment of failure". The JPWR1 jumper will take priority, meaning the default jumper location will always attempt to allow the Vault to power back on after power loss.</p>								
12	CMOS Battery Header	CE1	3V CR2032 (the battery is underneath the motherboard) connected via 2-pin connector (1.25mm pitch).								
13	Memory Slot	DIMM1	DDR5 SODIMM slot, supports up to a single 64GB SODIMM @4800MHz								
14	BIOS Programming Header	J1, J2	<p>Two separate headers used for BIOS programming. Based on the orientation in the image above, J1 is the top header and J2 is the bottom. (1x4, 2.00mm pitch each)</p> <p>J1:</p> <table border="1" data-bbox="722 1543 1404 1633"> <tr> <td>Pin 1: VDD</td> <td>Pin 2: HOLD#</td> <td>Pin 3: CLK</td> <td>Pin 4: SI</td> </tr> </table> <p>J2:</p> <table border="1" data-bbox="722 1696 1404 1759"> <tr> <td>Pin 1: CS#</td> <td>Pin 2: SO</td> <td>Pin 3: WP#</td> <td>Pin 4: GND</td> </tr> </table>	Pin 1: VDD	Pin 2: HOLD#	Pin 3: CLK	Pin 4: SI	Pin 1: CS#	Pin 2: SO	Pin 3: WP#	Pin 4: GND
Pin 1: VDD	Pin 2: HOLD#	Pin 3: CLK	Pin 4: SI								
Pin 1: CS#	Pin 2: SO	Pin 3: WP#	Pin 4: GND								

Item #	Object	Label	Description				
15	Switch for M.2 Modem Slot Behavior	SW1	<p>There are two switches labeled 1 and 2. The modem slot's (item #2) behavior is affected by these switches. By default, the switches are set to "Off." Protectli modems will work as expected in the default configuration. The switches' behavior is printed on the motherboard:</p> <p>1 off = PCIE (0V), 1 on = USB (1.8V) [<b>Switch 1 affects Pin 20 Voltage</b>]</p> <p>2 off = Standard (0V), 2 on = Reverse (3.3V) [<b>Switch 2 affects Pin 22 Voltage</b>]</p> <p>Some modems use Pins 20 and 22 for adding additional control functions like controlling airplane mode, resetting the modem, or are utilized as voltage sensing pins. Some modems will not work unless proper voltage is applied to specific pins. It is important to reference the documentation of your modem to verify if you need to utilize this switch.</p>				
16	CPU Fan Header	CPU_FAN1	<p>Four-pin PicoBlade-compatible header available for an optional PWM fan. Based on the image layout above, pin 1 is on the top. (1x4, 1.25mm pitch)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="padding: 2px;">Pin 1: Ground</td> </tr> <tr> <td style="padding: 2px;">Pin 2: VCC +12V</td> </tr> <tr> <td style="padding: 2px;">Pin 3: FG (Fan Tachometer / RPM Signal)</td> </tr> <tr> <td style="padding: 2px;">Pin 4: PWM (Pulse Width Modulation Control)</td> </tr> </table>	Pin 1: Ground	Pin 2: VCC +12V	Pin 3: FG (Fan Tachometer / RPM Signal)	Pin 4: PWM (Pulse Width Modulation Control)
Pin 1: Ground							
Pin 2: VCC +12V							
Pin 3: FG (Fan Tachometer / RPM Signal)							
Pin 4: PWM (Pulse Width Modulation Control)							
17	JNTP (NTP/i2c) Header	JNTP1	<p>Four-pin PicoBlade-compatible header used for NTP/i2c, commonly used for GPS capabilities, but offers many other solutions. (1x4, 1.25mm pitch)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="padding: 2px;">Pin 1: i2c0_SCL</td> </tr> <tr> <td style="padding: 2px;">Pin 2: i2c0_SDA</td> </tr> <tr> <td style="padding: 2px;">Pin 3: +3.3V</td> </tr> <tr> <td style="padding: 2px;">Pin 4: Ground</td> </tr> </table>	Pin 1: i2c0_SCL	Pin 2: i2c0_SDA	Pin 3: +3.3V	Pin 4: Ground
Pin 1: i2c0_SCL							
Pin 2: i2c0_SDA							
Pin 3: +3.3V							
Pin 4: Ground							

## Thermal Pads and Screws Information



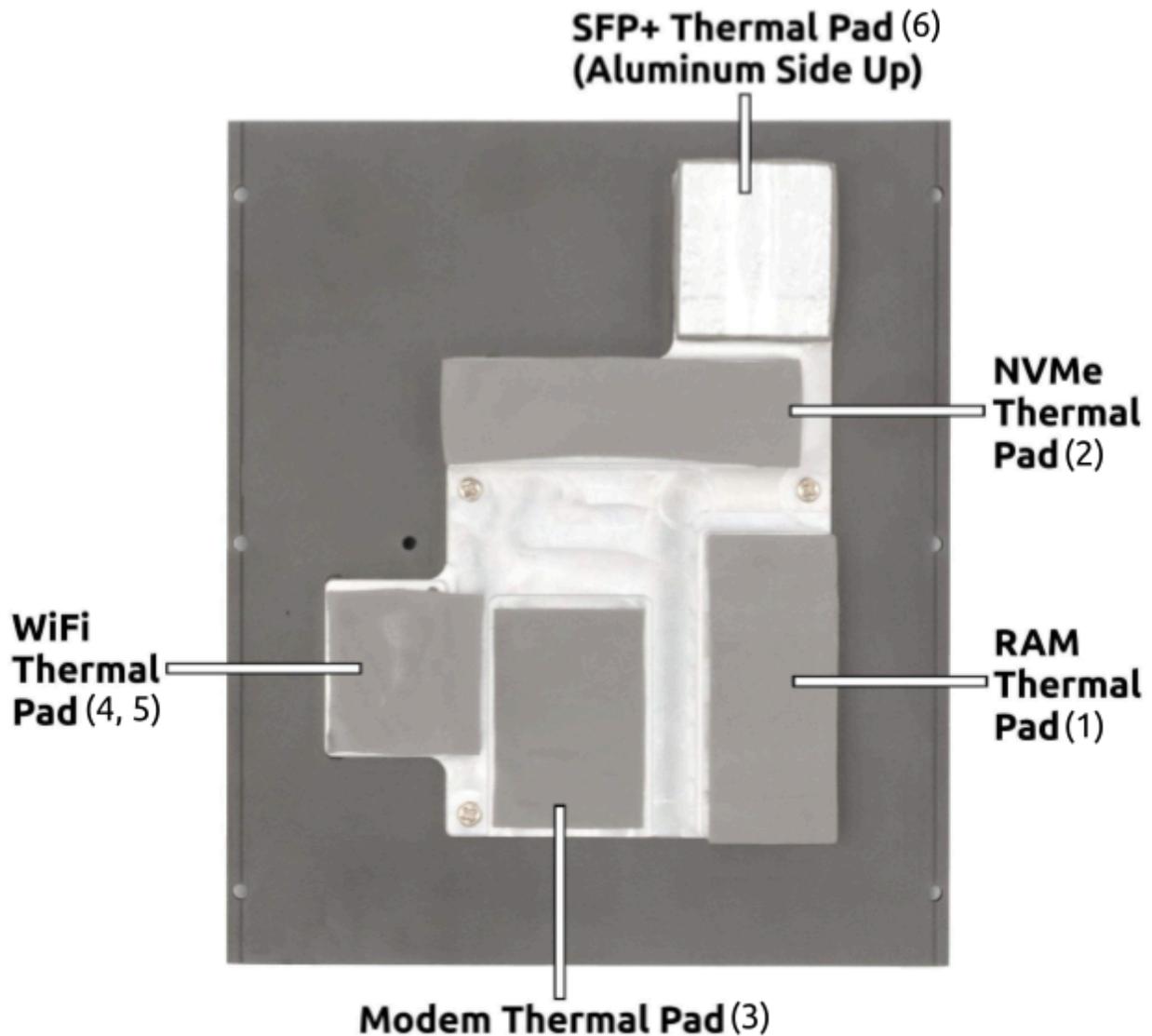
Item #	Object	Description
1	RAM Thermal Pad	Placed between the RAM and the heatsink. Dimensions: 60mm (L) x 25mm (W) x 3mm (H)
2	NVMe Bottom Thermal Pad	Placed between the NVMe and the heatsink. Dimensions: 68mm (L) x 20mm (W) x 3mm (H)
3	4G/5G Modem Thermal Pad	Placed between the 4G/5G Modem and large heatsink. Dimensions: 44mm (L) x 30mm (W) x 1mm (H)

4	WiFi Module Thermal Pad 1	Placed between the WiFi card and large heatsink. Two thermal pad sizes have been supplied to account for different sizes of WiFi cards. Dimensions: 34mm (L) x 30mm (W) x 3mm (H)
5	WiFi Module Thermal Pad 2	Placed between the WiFi card and large heatsink. Two thermal pad sizes have been supplied to account for different sizes of WiFi cards. Dimensions: 32mm (L) x 30mm (W) x 2mm (H)
6	SFP+ Thermal Pad	Placed between the SFP+ cages and the heatsink. Includes an aluminum sheet to prevent thermal compound from entering the SFP+ cages while maintaining effective contact. Dimensions: 34mm (L) x 30mm (W) x 3mm (H)
7	M2 Screws	Used to secure internal computer components (Wifi, LTE, NVMe, etc.) to the standoffs on the motherboard. When a Vault is purchased through protectli.com, these screws are used to install the components selected during checkout. Dimensions: Rounded head, M2 screw size, 4.75mm length, 1.9mm thread diameter.
8	Extra Thermal Pads	Leftover thermal pads. Can be used in miscellaneous spots or to patch up sections of other thermal pads that may have become damaged.

## Heatsink and Thermal Pads

The VP2440 heatsink comes mounted to the bottom chassis plate. Customer removal or installation of the heatsink is not required.

The image below shows the heatsink with all thermal pads installed. Reference numbers are defined in the "Thermal Pad and Screws Information" table. The SFP+ thermal pad comes with an aluminum sheet on one side. Do not remove it. The aluminum sheet side of the SFP+ thermal pad must face and make contact with the SFP+ cages to prevent thermal compound from entering the cages.



# Dimension View

External chassis dimensions: 170mm (L) x 152mm (W) x 53mm (H)



# Document History

2026-02-27

- Removed the "Optional WiFi" and "Optional LTE Cellular" rows in the Technical Specifications due to the same information being present in other locations on the datasheet, and to cut down on confusion

2025-10-21

- Clarified behavior of RS-232 Serial Header (Item #7 in Internal Interfaces) to imply that it is not strictly used for console output. This header can be used for general serial output/input. Emphasized what settings to change in the firmware to strictly activate console output.

2025-08-20

- Initial document